

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

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Title of Invention	METHOD OF FORMING FREESTANDING SEMICONDUCTOR LAYER																																															
<p>Application Number: Confirmation Number: First Named Applicant: Brent Anderson Attorney Docket Number: BUR920030031US1 Search string: (5723370 or 6492212 or 20020181523).pn.</p>																																																
<h3>US Patent Documents</h3> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td></td><td>1</td><td>5723370</td><td>1998-03-03</td><td>Ning et al</td><td></td><td></td><td></td></tr><tr><td></td><td>2</td><td>6492212</td><td>2002-12-10</td><td>Ieong, et al</td><td></td><td></td><td></td></tr></tbody></table> <h3>US Published Applications</h3> <p>Note: Applicant is not required to submit a paper copy of cited US Published Applications</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Pub. No.</th><th>Date</th><th>Applicant</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td></td><td>1</td><td>20020181523</td><td>2002-12-05</td><td>Pinneo, et al</td><td></td><td></td><td></td></tr></tbody></table> <p>Signature</p> <table border="1"><tr><td>Examiner Name</td><td>Date</td></tr><tr><td></td><td></td></tr></table>					init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass		1	5723370	1998-03-03	Ning et al					2	6492212	2002-12-10	Ieong, et al				init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass		1	20020181523	2002-12-05	Pinneo, et al				Examiner Name	Date		
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